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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10088044	FILING DATE 03/14/2002	CLASS 39.4 359	SUBCLASS :	GAU 2872	EXAMINER LEDYNH BOL LEDYNH
**APPLICANTS: Duance Roger;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A 371 OF PCT/AU00/01101 09/15/2000					
** FOREIGN APPLICATIONS VERIFIED: AUSTRALIA PQ-2858 09/16/1999					
PG-PUB		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no		BRI-00062	
Verified and Acknowledged Examiners's initials					
TITLE : Sensor memory electronic circuit					
U.S. DEPT. OF COMM./PAT. & TM. PTO-436L (Rev. 12-94)					

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
ISSUE FEE		Sheets Drwg.	Figs.Drwg.
Amount Due	Date Paid	Print Fig.	
Primary Examiner		Applicati n Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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